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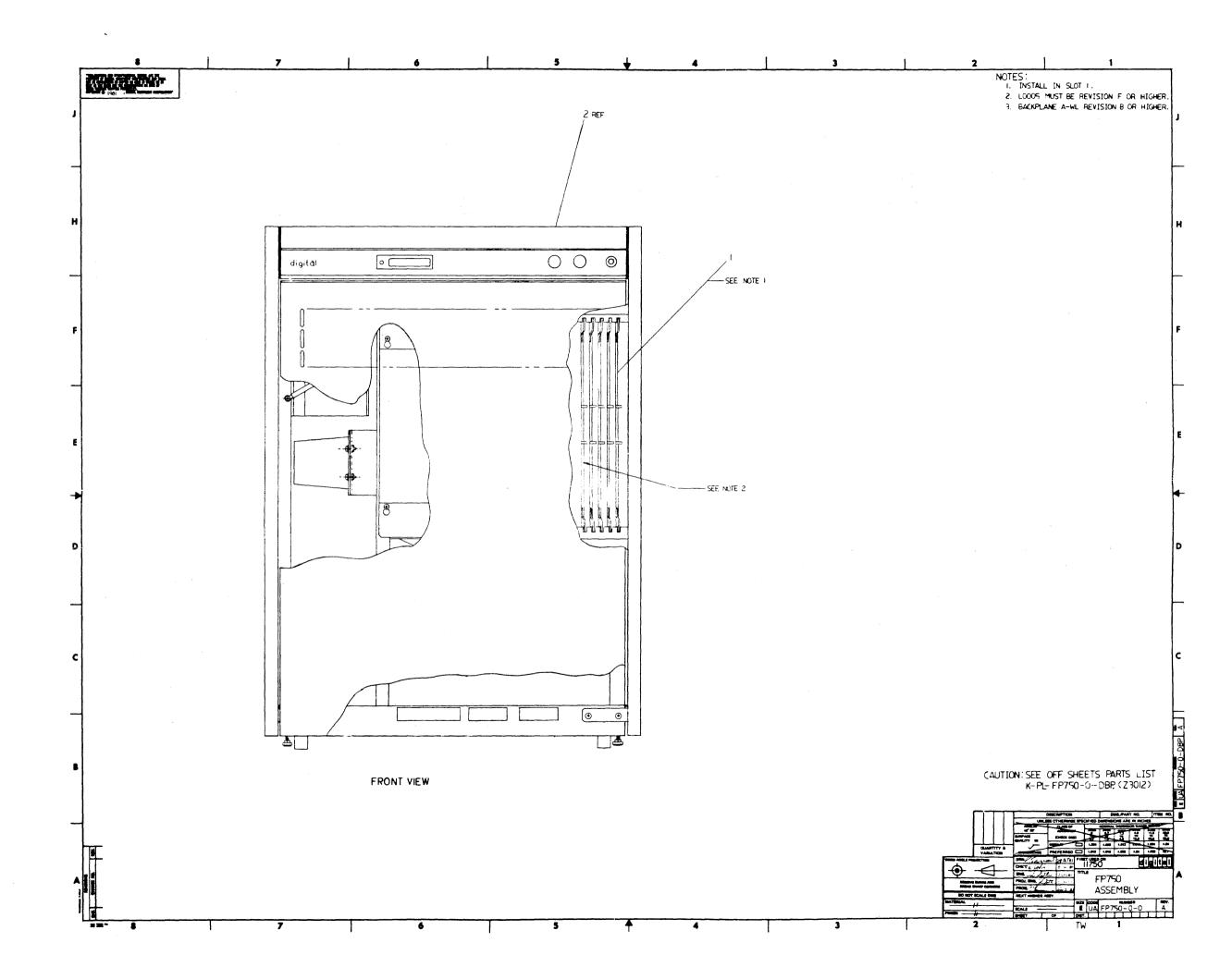
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UNIT VARIATIONS

COVERED BY THIS
PRINT SET

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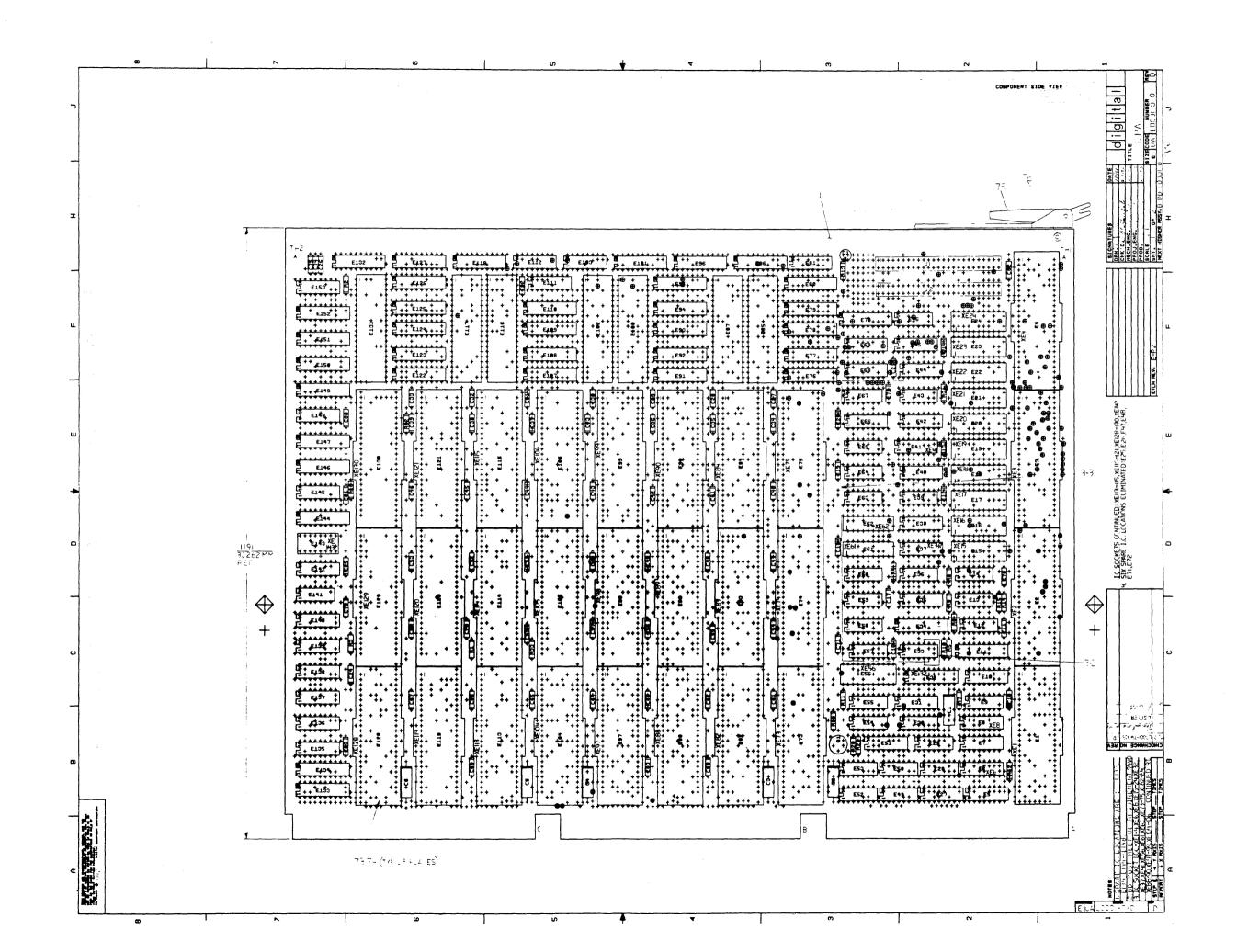
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1	L0001-00 11750-AA 3700658-01	FLOATING POINT ACCELERATOR KA750, MS750-AA, TU58-AA, DZ11-B, DD PKG. PROCESSOR FF750	REF REF	

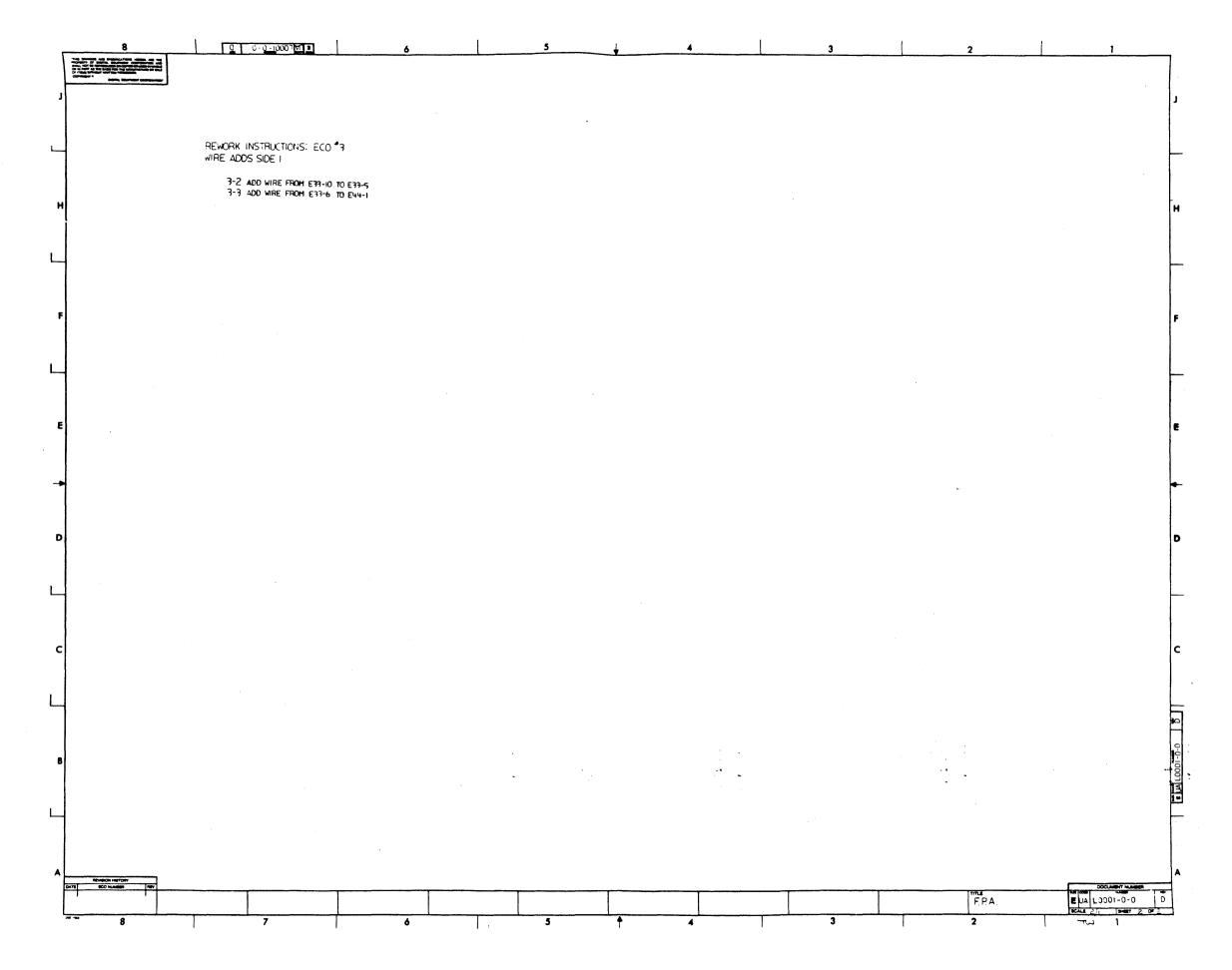
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E. B DD | 000 - 0-0 DRAWING NO. PART NO. DESCRIPTION **REVISIONS** ABCDEI MODULE REVISION B-DD-L0001-0-0 2 ABCDE FPA DRAWING DIRECTORY E-UA-L000 1-0-0 2 ABCDD FPA UNIT ASSEMBLY K-PL-L0001-0-DBP 3 FPA PARTS LIST ABBQD EMD-50138800-0 5 FPA DRILL & ETCH DRAWING A A A B B 5013880 D D D EP3EP2 **IETCH BOARD** K-PC-L0001-0-DBI FPA DESIGN DATA BASE IDEAD DEPREPE ABCDD E-EC-5013880-0-0 | 3 FPAETCH CUT DRAWING FPA DESIGNDATA BASE SUDSA BCDE K-CS-L0001-D-DBS FRACTION I/O 63-48 & 31-16 AAAAA D-CS-L0001-0-1 FRACTION I/O 47-32 & 15-0 AAAAA D-CS-L0001-0-2 * COARSE SHIFTER AAABB D-CS-L0001-0-3 NS BUS TO ALUBUFFERS Ø-31 A A A A lD-CS - L0001-0-4 INS BUS TO ALU BUFFERS 32-63 A A A A A D-CS-L0001-0-5 | 1 FRACTION ALU (BYTES 7 TO WALA A A D-CS-10001-0-6 FRACTION ALU (BYTES 3 TO Ø) A A A B B D-CS-L0001-0-7 | 1 D-CS-L0001-0-8 MULTIPLIER REGISTER AAAAA PPROD MULTIPLIERS MAND AAAAA D-CS-L0001-0-9 | 1 BYTES 1-3 AA D-C S-L0001-0-10 1 PPROD MULTIPLIERS MAND A A A B B BYTES 4-7 D-CS-L0001-0-11 1 * PARTIAL PRODUCT ADDERS A A A A PARTIAL PRODUCT ADDER CLASA A A A A D-CS-L0001-0-12 1 * PARTIAL PRODUCT REGISTER A A A A D.(S.L0001-0-13 | * D-CS-L0001-0-14 1 * AAAAA ALIGN NORMALIZE, SHIFT ILOGIC: EXPC NOTES: *CONTROL SOURCE IS DATA BASE NO CONTROLLED PAPER ORIGINALS EXIST 12/81 TWOC | 3-82TW002 |0-82TW003 |1-84 TW004 REVISIONS CHG NO. DATE FP 750 TITLE THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PRO-PERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL FPA NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF RCY. SIZE CODE NUMBER ENG. ITEMS WITHOUT WRITTEN PERMISSION. **B** | **DD** | L0001-0-0 PROD. PaHeison LNL 9-21-81 COPYRIGHTS | 98 | DIGITAL EQUIPMENT CORPORATION SHEET | OF 2

B DD SISE CODE L0001-0-0 DRAWING NO. PART NO. DESCRIPTION **REVISIONS** D-CS-L0001-0-15 1 * EXPONENT LOGIC, SHORT LIT A A A A A LATCH SIGN LOGIC, EXP ZERO, RES ABCCC D-CS-L0001-0-16 1 * OP DETECT D-CS-L0001-0-17 1 * OPCODE ENTRY, STEP, CTR, AABBB BRANCH LOGIC D-CS-L0001-0-18 1 * CONTROL STORE BYTES 9 TO 5 A A A A D-C5-L0001-0-19 1 * CONTROL STORE BYTES 4TO Ø A A A A AAAAB D-CS-L0001-0-20 1 * CLOCK LOGIC CONDITION CODES FPA PRESENTIALA A A A D-CS - L0001-0-21 AAABB PWR-GND DECOUPLING D-CS-L0001-0-22 1 * MIC RO DIAG TEST CONNECTORS A A A A A D-CS-L0001-0-23 1 * D-CS-L0001-0-24 | * FORWARD REFERENCE AAAAA AAABB D-CS-L0001-0.25 | * FORWARD REFERENCE FORWARD REFERENCE ABBBB D-C5-L0001-0.26 1 * AAAAA FORWARD REFERENCE D-C5-L0001-0-27 | * ABCCC D-C5-L0001-0-28 | * FORWARD REFERENCE D-CS-L0001-0-2911 * FORWARD REFERENCE AAAAB ΑΑΑΑΑ D-CS-L0001-0-30 FORWARD REFERENCE NOTES: * CONTROL SOURCE IS DATABASE 12/81 TWOO! 3-82 TWOO2 10-82 TWOO3 1-84 TWOO4 NO CONTROLLED PAPER ORIGINALS **EXISTS** USED ON OPTION/MODEL FP750 DRN "THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PRO-PERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL CHK Of Marion F. World FPA NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF SIZE CODE NUMBER

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INE ITEM TOP DOCUM	ENT	PART NUMBER REV	DESCRIPTION			OO PER VAR			NCE DESIGNA	A TOR	
			VARIA	TION REVISION L	EVEL:	E 1			THE BESTON	1100	
1 1		5013880-00	LOOO1 MULTI	AYER BOARD		1			•		
2 2		1012784-00	.047 MFD	50V +80-20%	CER	70		C10-C1	4,C16-C75,	079 00	2
3 3		1114384-00	• •	5MA GREEN	02.11	1		D1	4,016-075,0	L/9-L8	3
4 4		1302379-00		25 W 5.0 %	CF	4		R30-R3	3		
5 5		1300295-00		25 W 5.0 %	CF	1		R21	3		
6 6		1300316-00		25 W 5.0 %	CF	4		R1-R4			
7 7		1300365-00		25 W 5.0 %	CF	6		R5-R10			
8 8		1300447-00	4.70 K	25 W 5.0 %	CF	2		R14,R1			
9 9		1301401-00	750.0 .:	25 W 5.0 %	CF	1		R20	,		
10 10		1301775-00		25 W 5.0 %	CF	1		R11			
11 11		1503121-00		N 350MW SI	Ν	1		01			
12 12		1912728-00	74\$251	MUX 1 OF 8 TR	I-STA	1		E40			
13 13		1910532-00		NAND GATE-QUAD		8		E12,E1	4,E50,E58.	E59,E6	3,E64,E4
14 14		1910533-00		NAND GATE-QUAD	0, NIS	1		E30			
15 15		1910534-00		INVERTER GATE-H		3		E33,E5	3,E67		
16 16		1910539-00		NAND GATE-DUAL		1		E49			
17 17		1910542-00		A-O-I GATE 4-2-		2		E28,E2	29		
18 18		1910544-00		FF-D DUAL, EDGE		1		E60			
19 19		1910545-00		FF-JK DUAL,EDGE		1		E51			
20 20		1910547-00		MUX 1 0F 4 (E		3			9,E142		
21 21		1910548-00	74S157	MUX 1 0F 2 (6	IUAU)	9	CONT		31,E66,E68,	E136,E	.135,E137
22 22		1910550-00	74\$174	רר ה ארע		1	CONT		140		
23 23		1910552-00		SHIFT REG.,4BIT	DICH	1 2		E5) <u></u>		
24 24		1910957-00		FF-D QUAD COMMO		1		E44,E4 E27	+5		
25 25		1911675-00		DECODER/DEMUX 3		1		E7			
26 26		1911712-00		AND-OR GATE-INV		1		E43			
27 27		1911983-00		NAND GATE-POSIT		1		E139			
28 28		1912097-00		LOOK AHD CARRY		5			103,E111,E1	12.F15	53
29 29		1912389-00		AND GATE-QUAD 2		2		E54,E6		,	,
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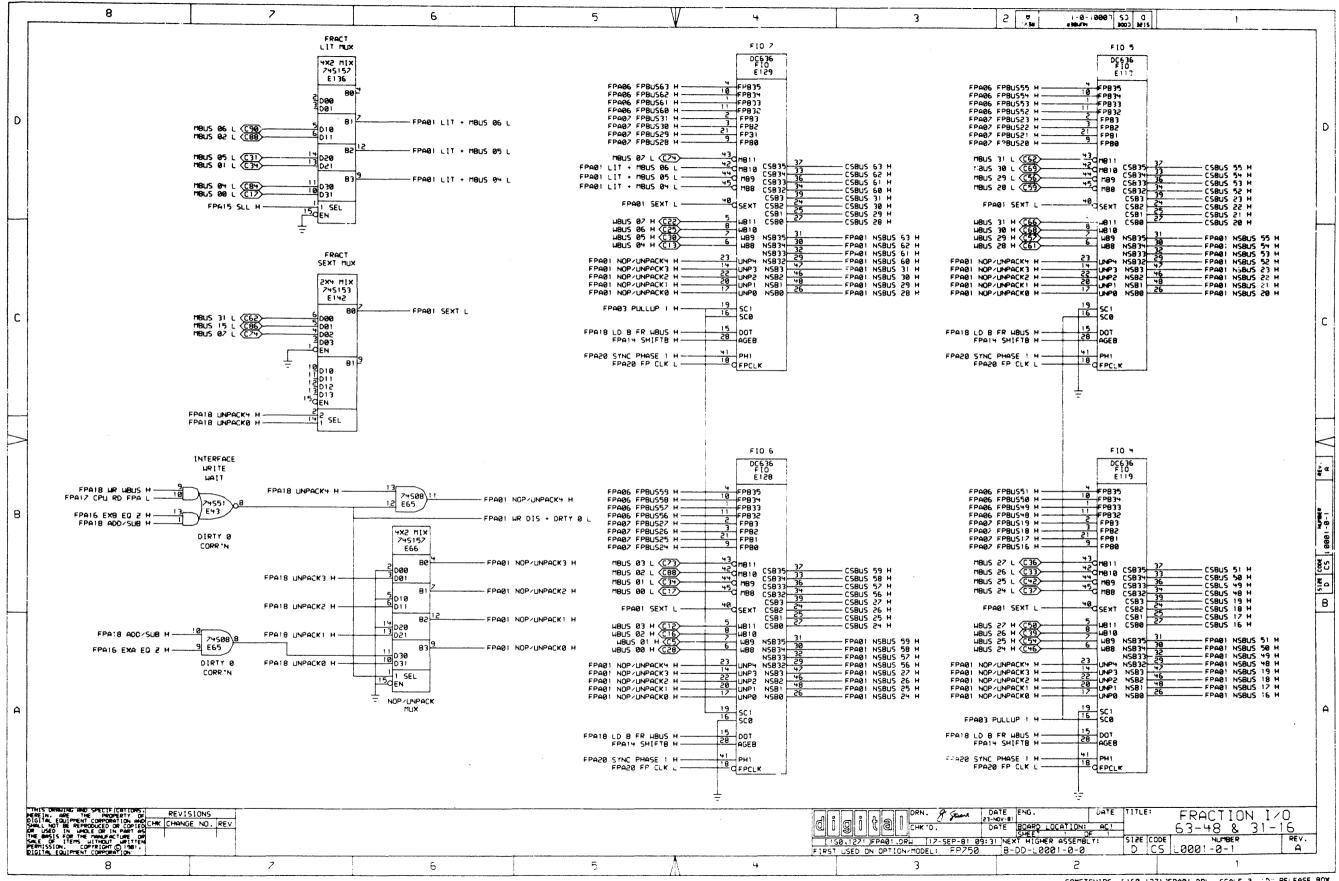
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30 30	1912660-00	7	45253	MUX 1 OF 4 (DUAL)	1		E35		
31 31	1912666-00			BUFFER GATE-QUAD 21N	3		–	39,E57	
32 32	1912746-00	DEC	74537	NAND GATE-QUAD 2IN	1		E52	139,697	
33 33	1913340-00			OR GATE-QUAD 21N	1				
34 34	1913462-00				1		E36		
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39 39	1914710-00	DC					E90,E		_
3) 3)	1514710-00	DC	636	BIPOLAR, LS, 400-GATE	8			E82,E104,E113,E114.	E119,
0						CONT			
40 40	1914711-00	DC	637	BIPOLAR, LS, 400-GATE	4		E83,E	E89,E105,E98	
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51 51	23074B1-00	B1-01			1		_		
52 52					1		E32		
52 52	23056F2-00	F2-01			1		E61		
53 53	23057F2-00	F2-01			1		E62	~	
54 54	1017472-00	10	MFD	35V +75-10% AL EL	6		C1-C6	>	
55 55	23043D2-00	D2-01			1		E15		
56 56	23044D2-00	D2-01			1		E16		
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62 62					1		E23		
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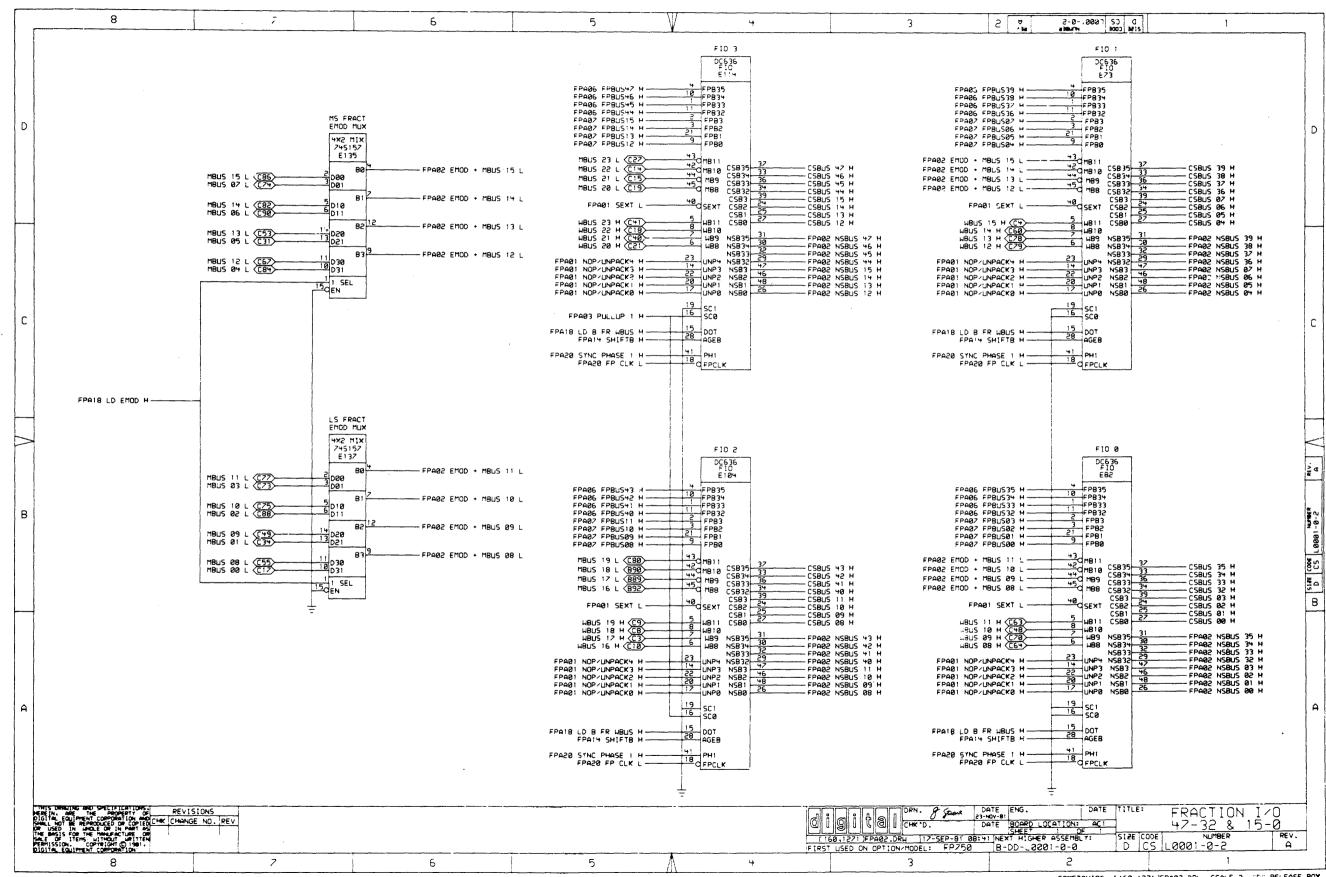
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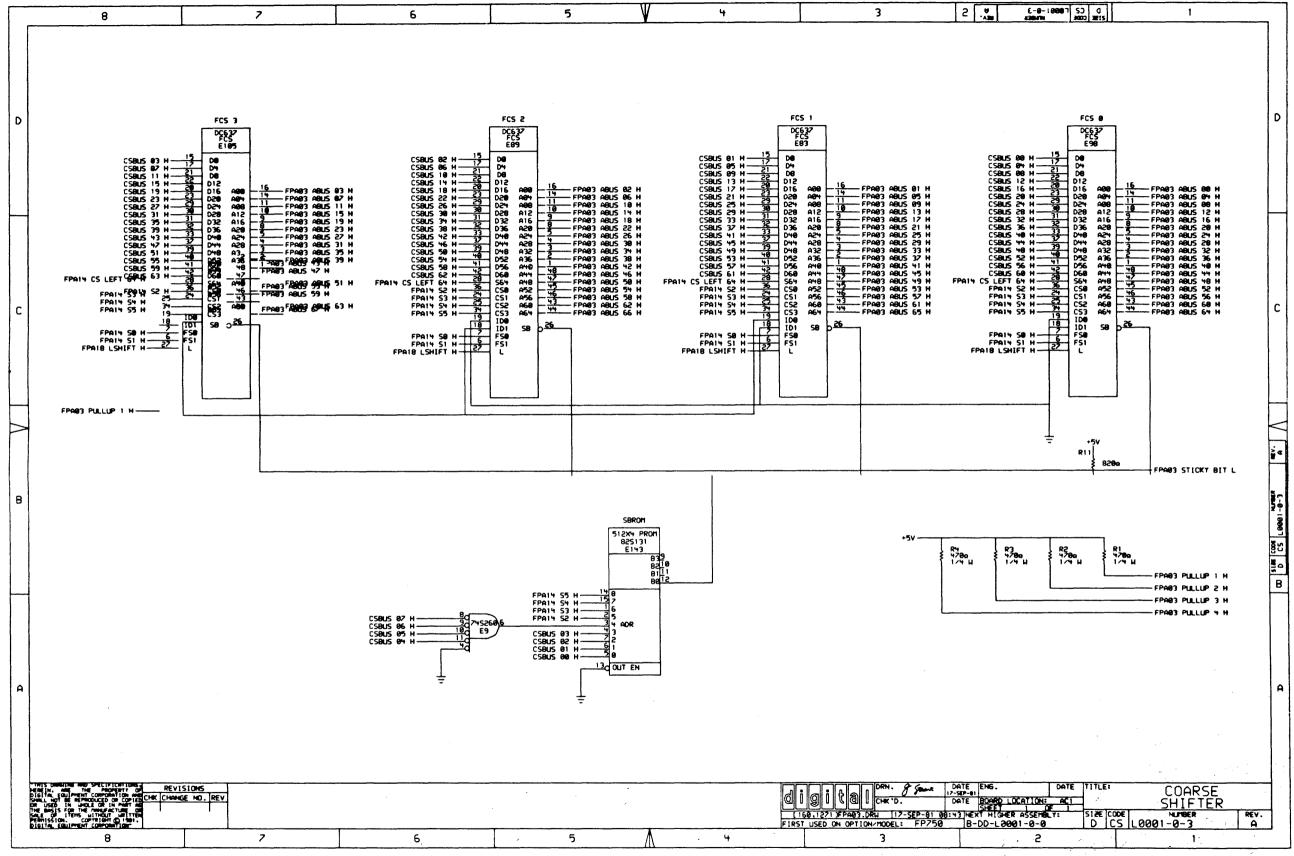
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	75	75		1216988-02		HANDLE, MODULE, HEX	TWO EJECTORS	1		
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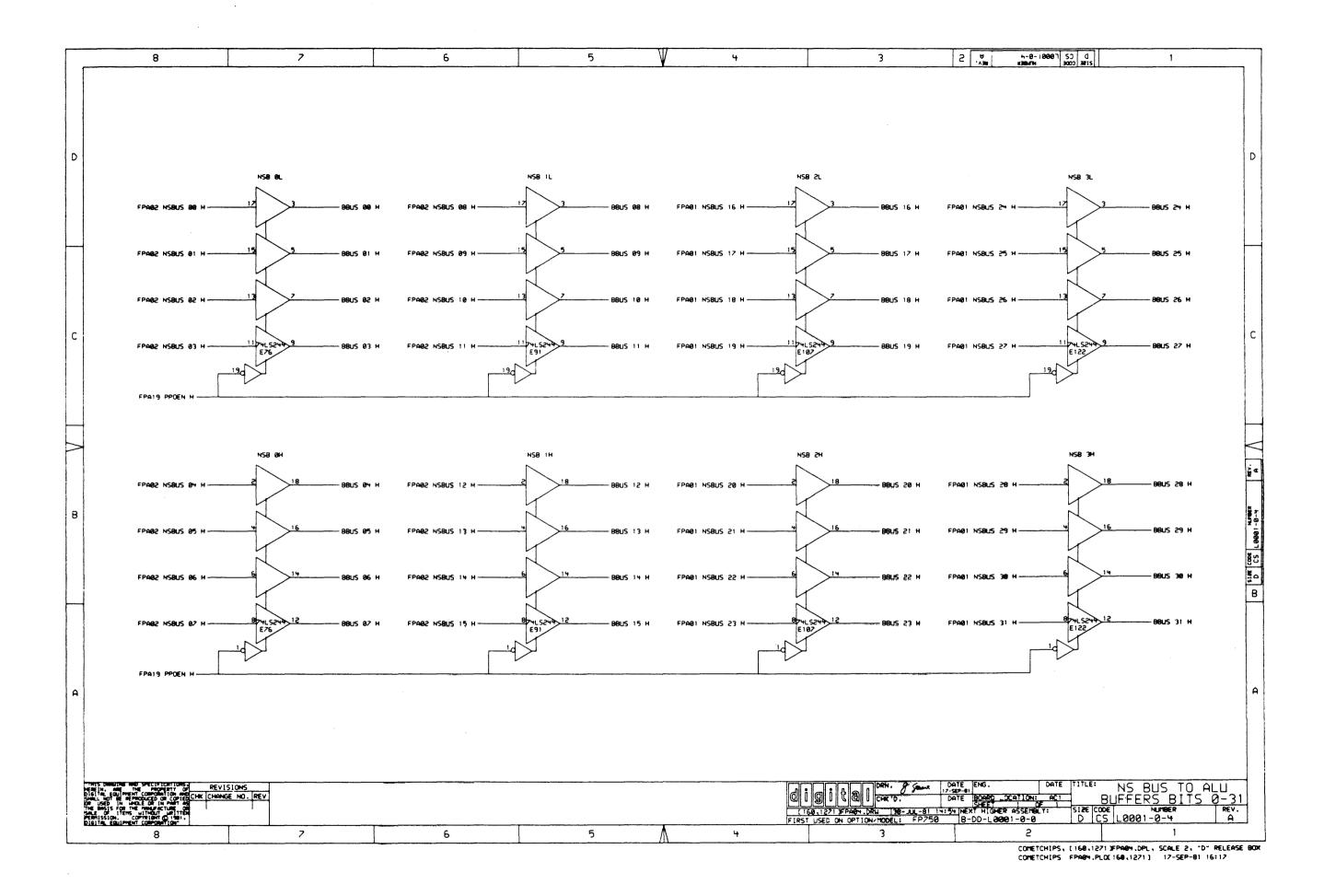
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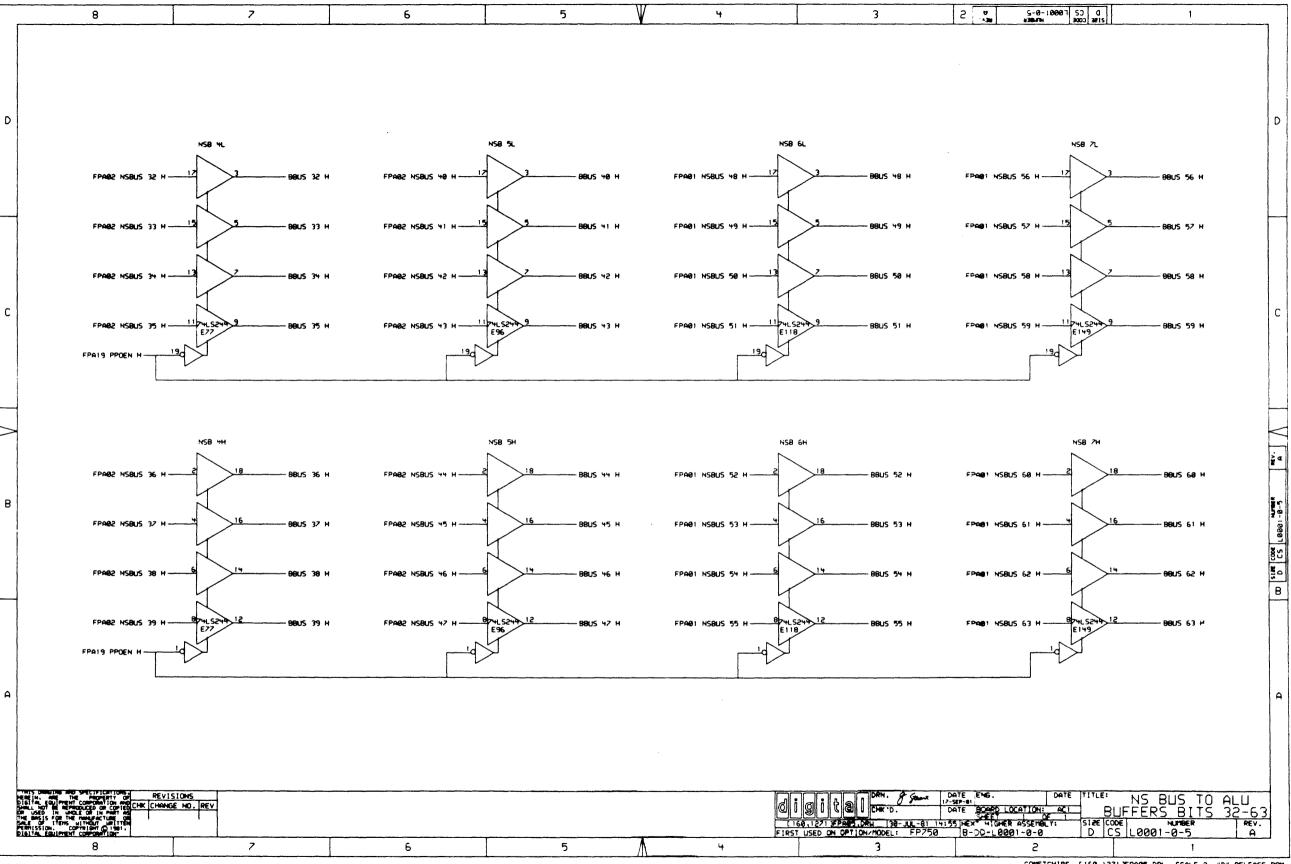
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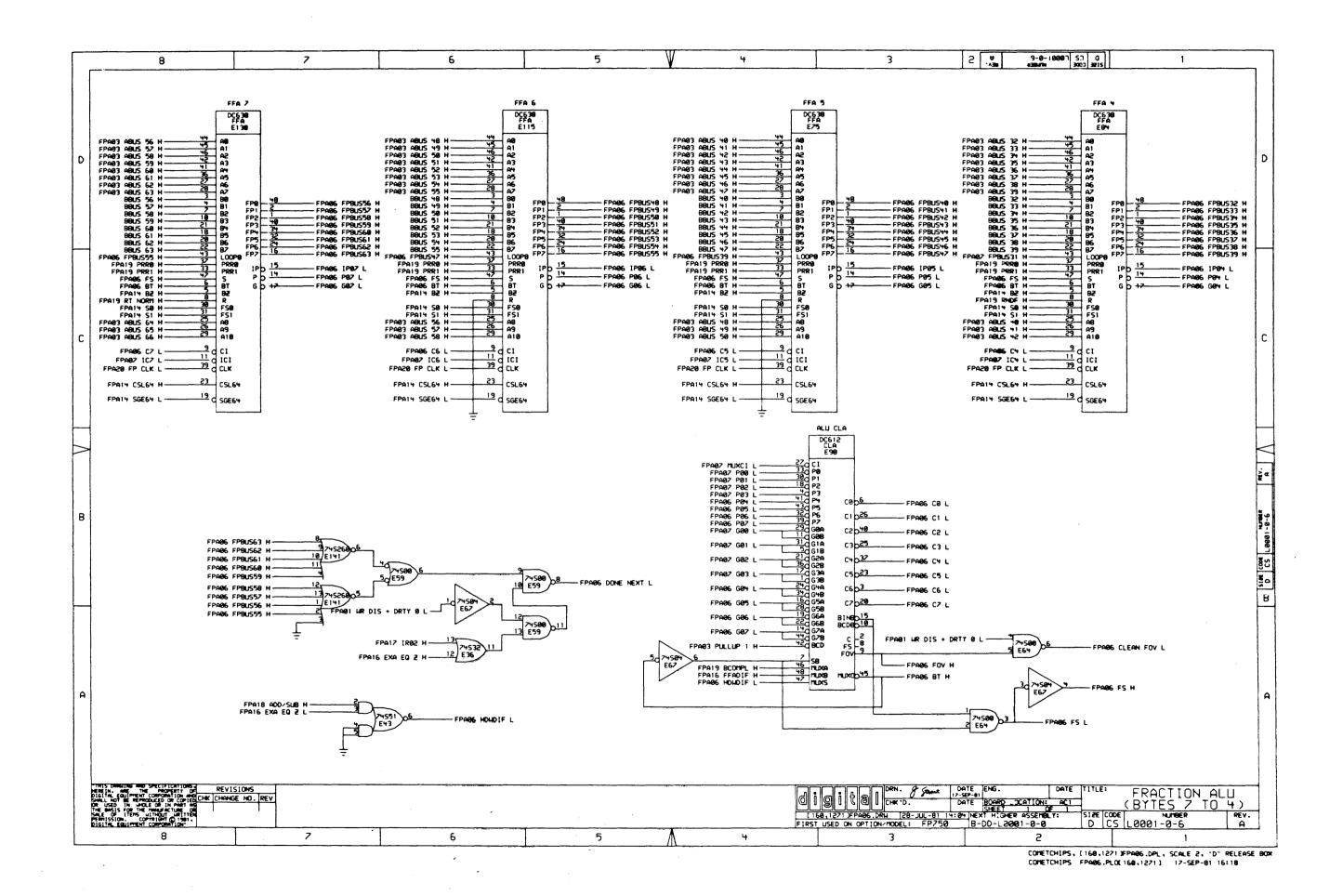


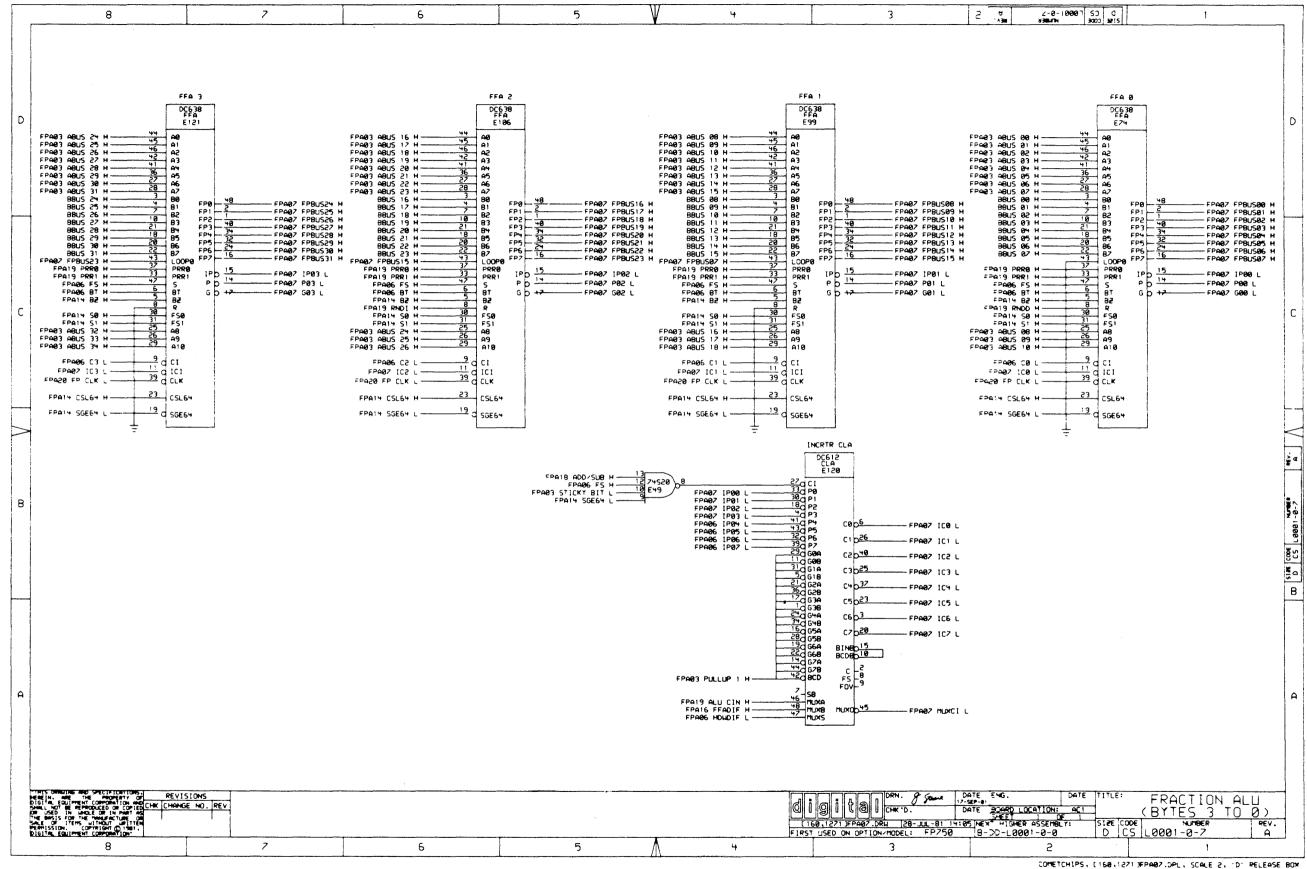


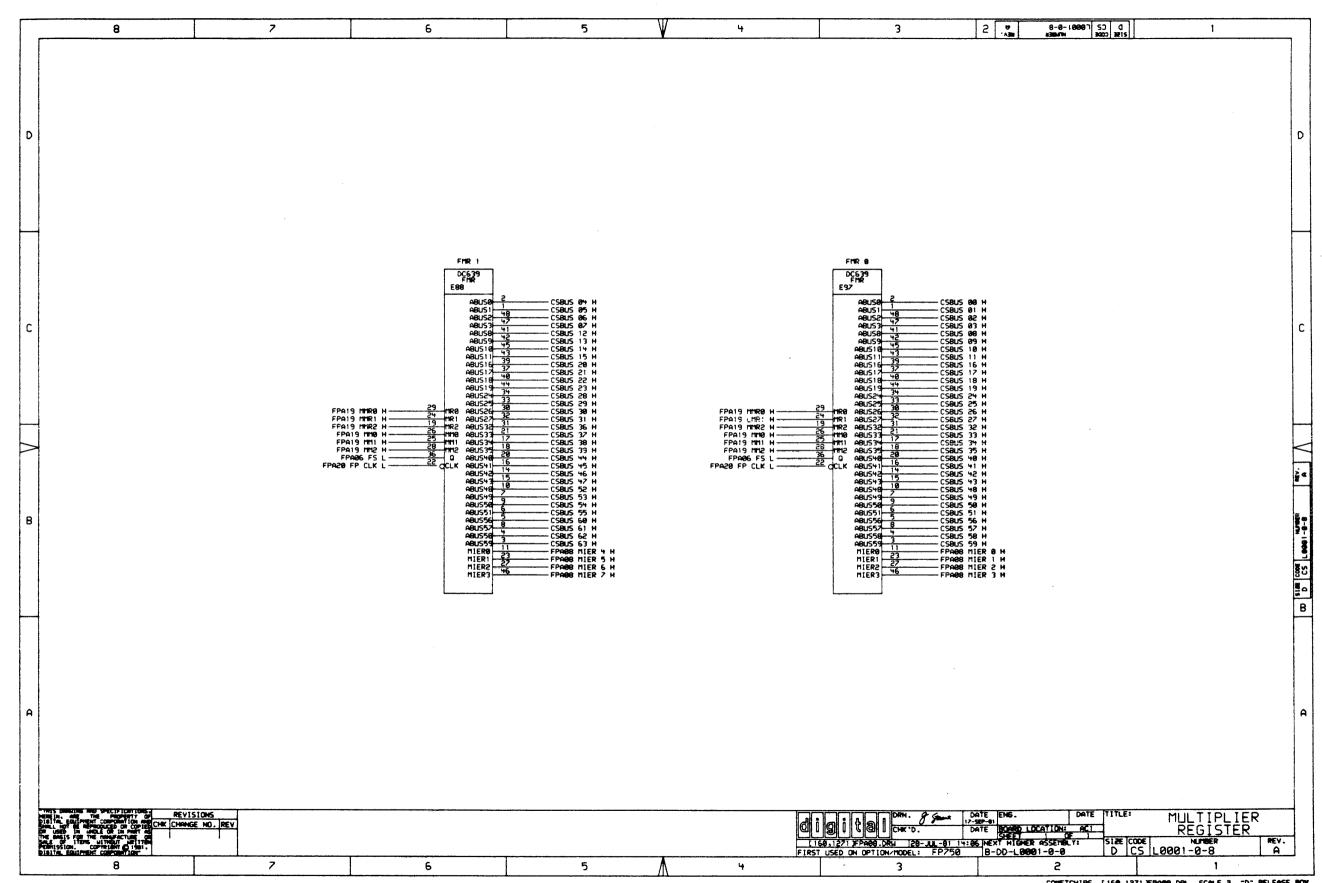


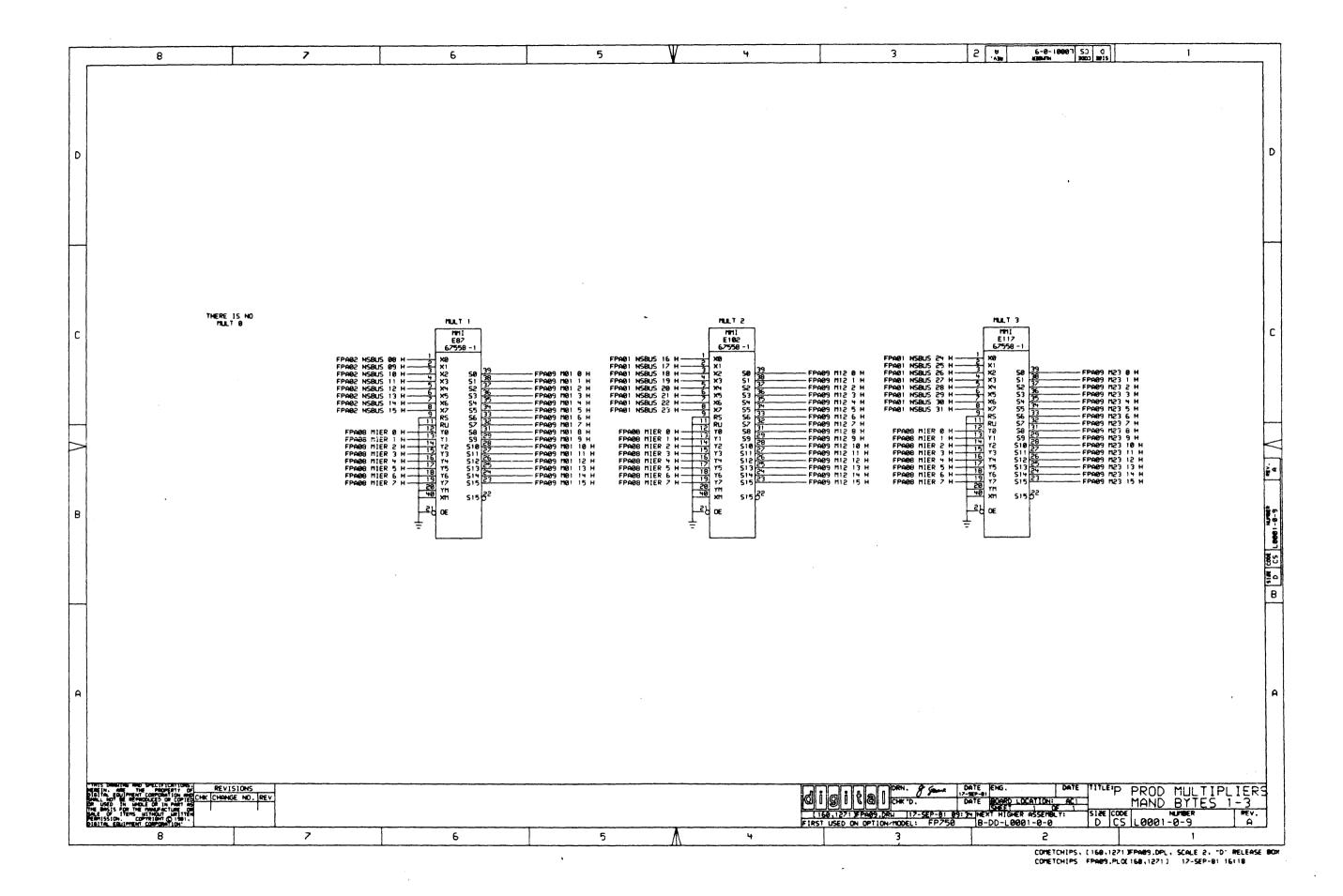


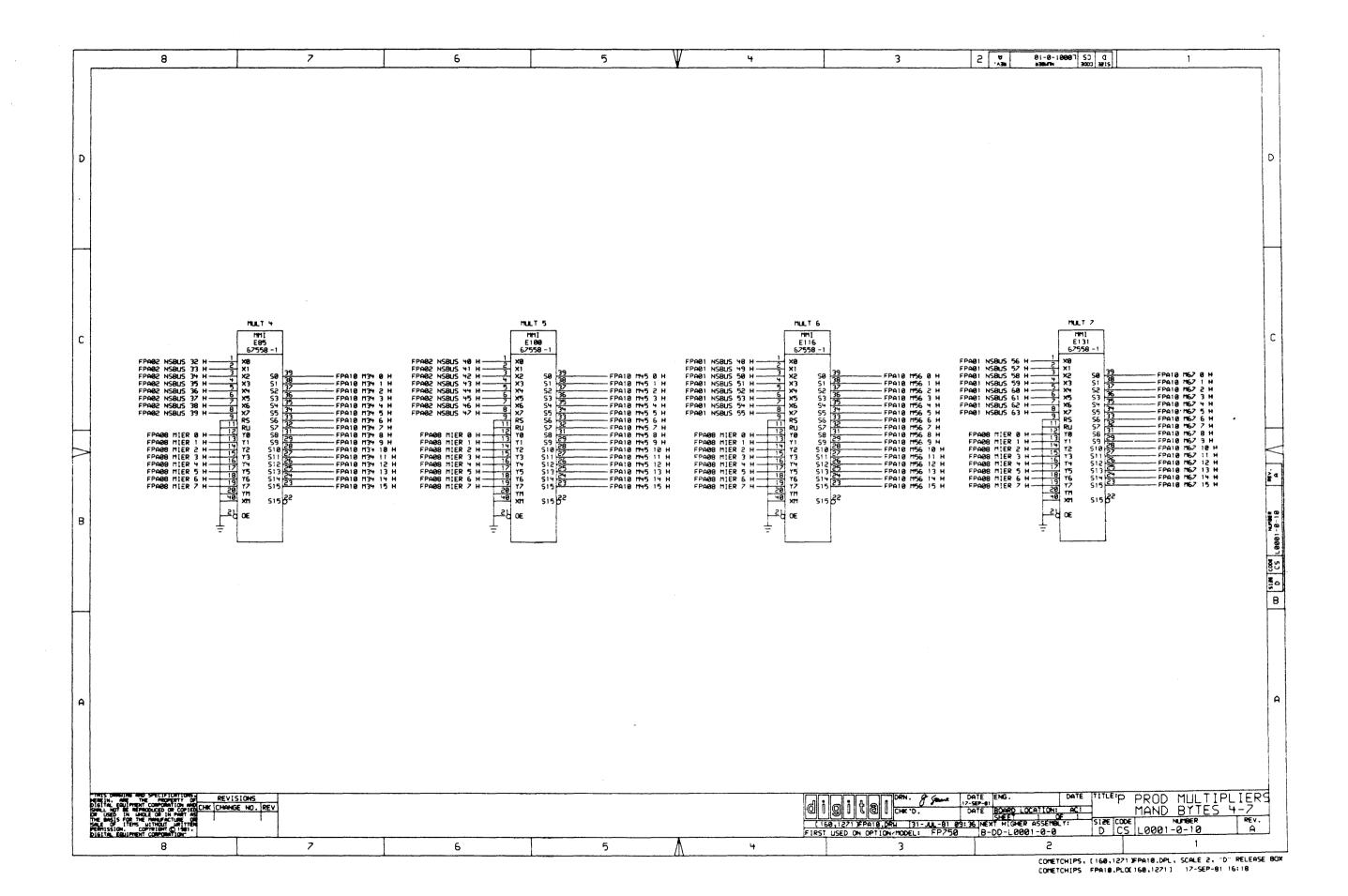
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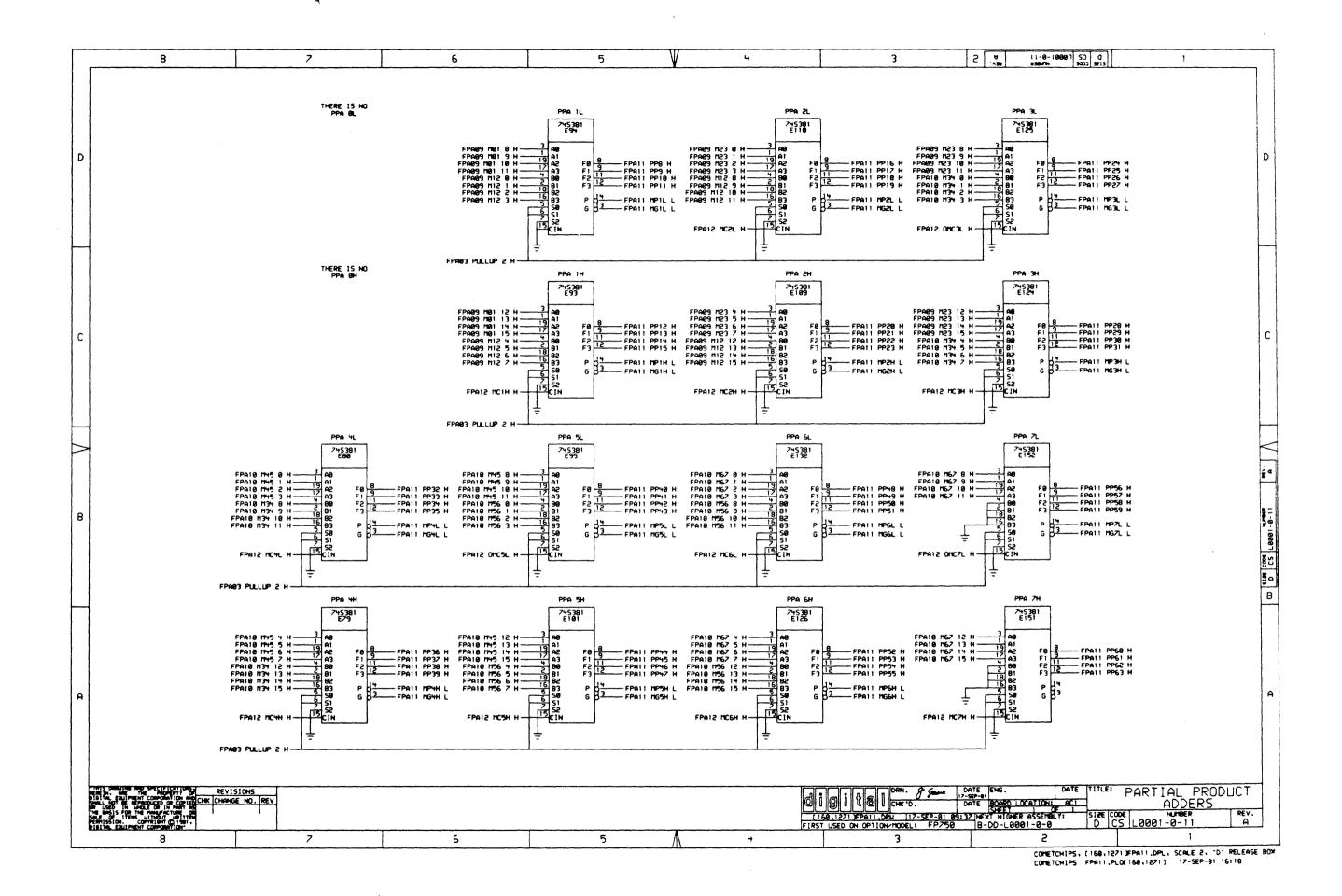


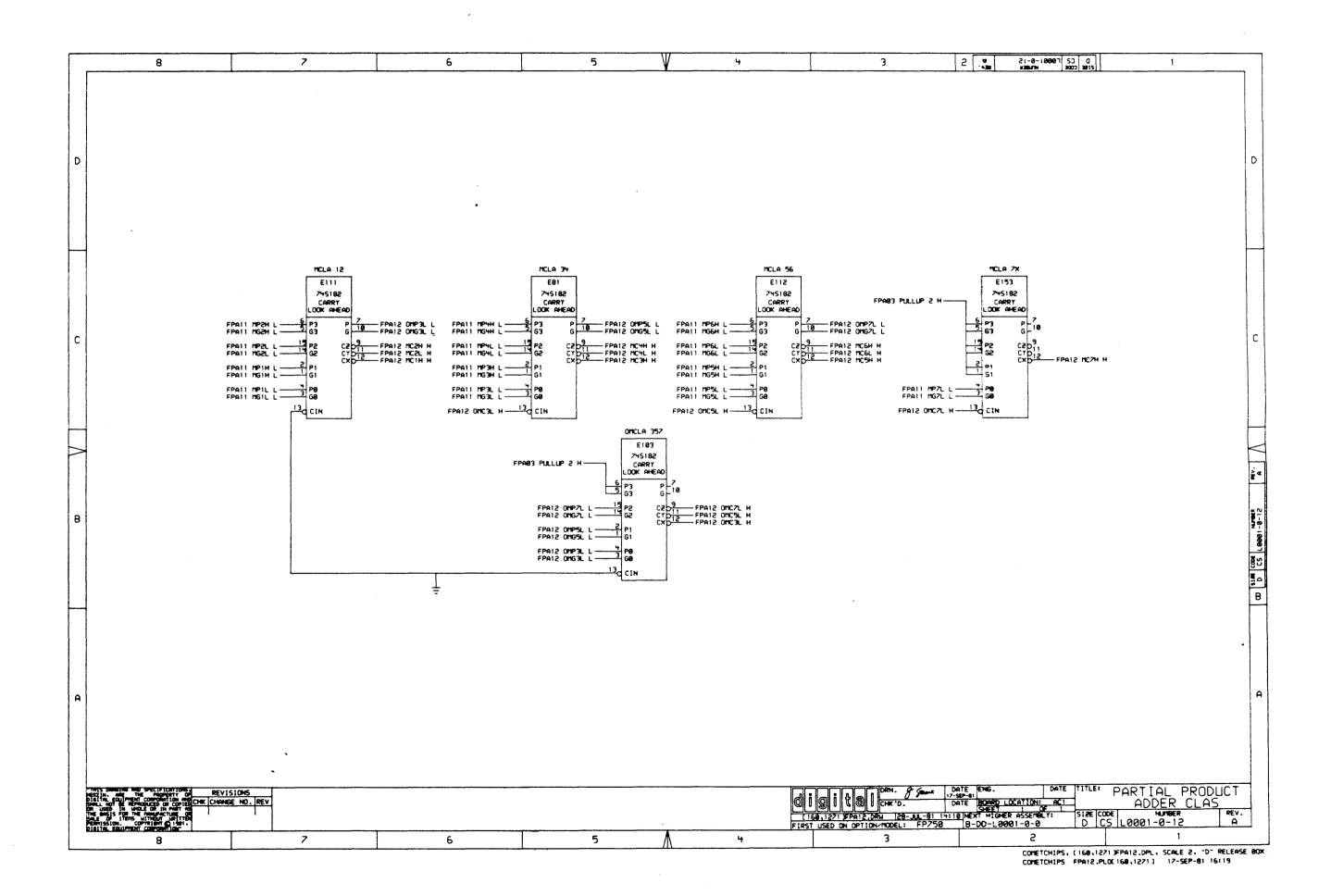


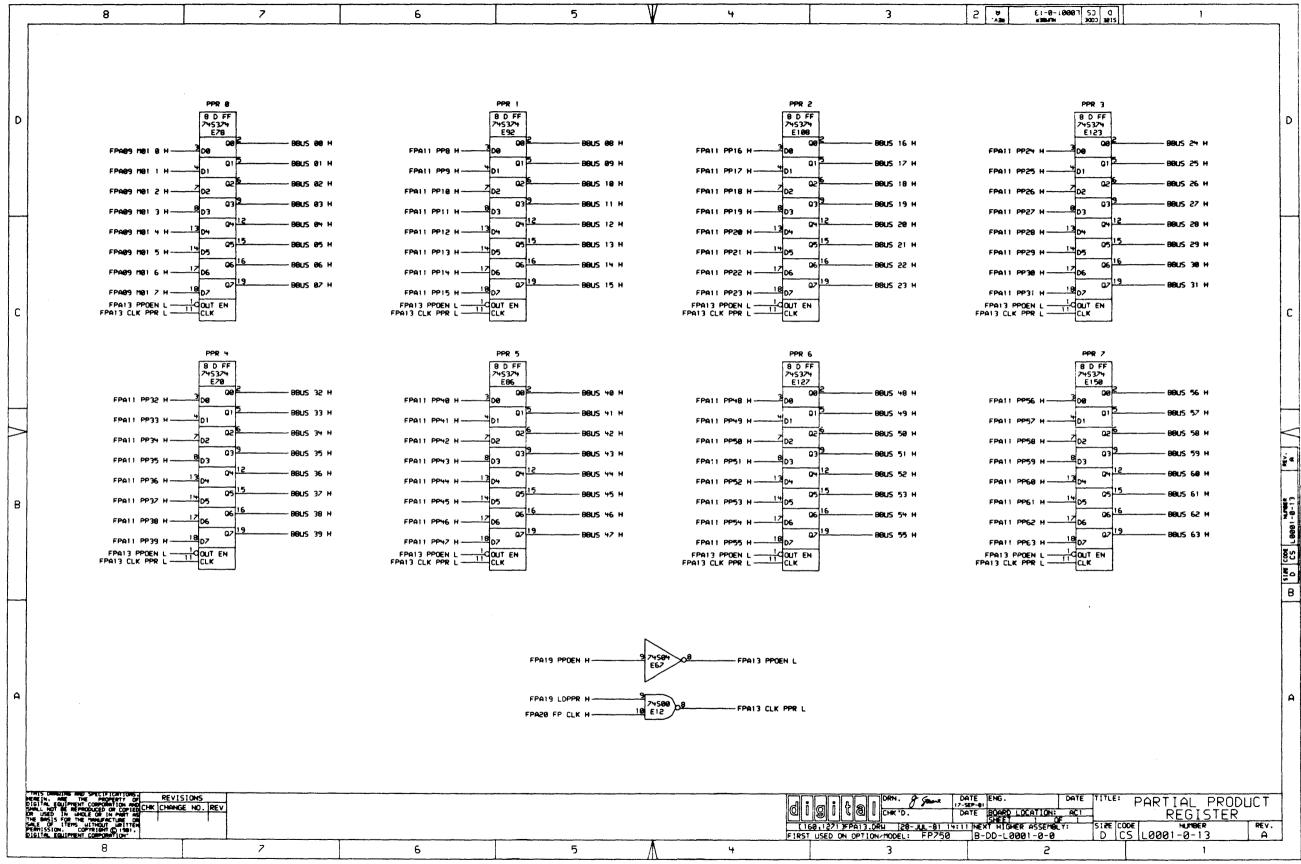


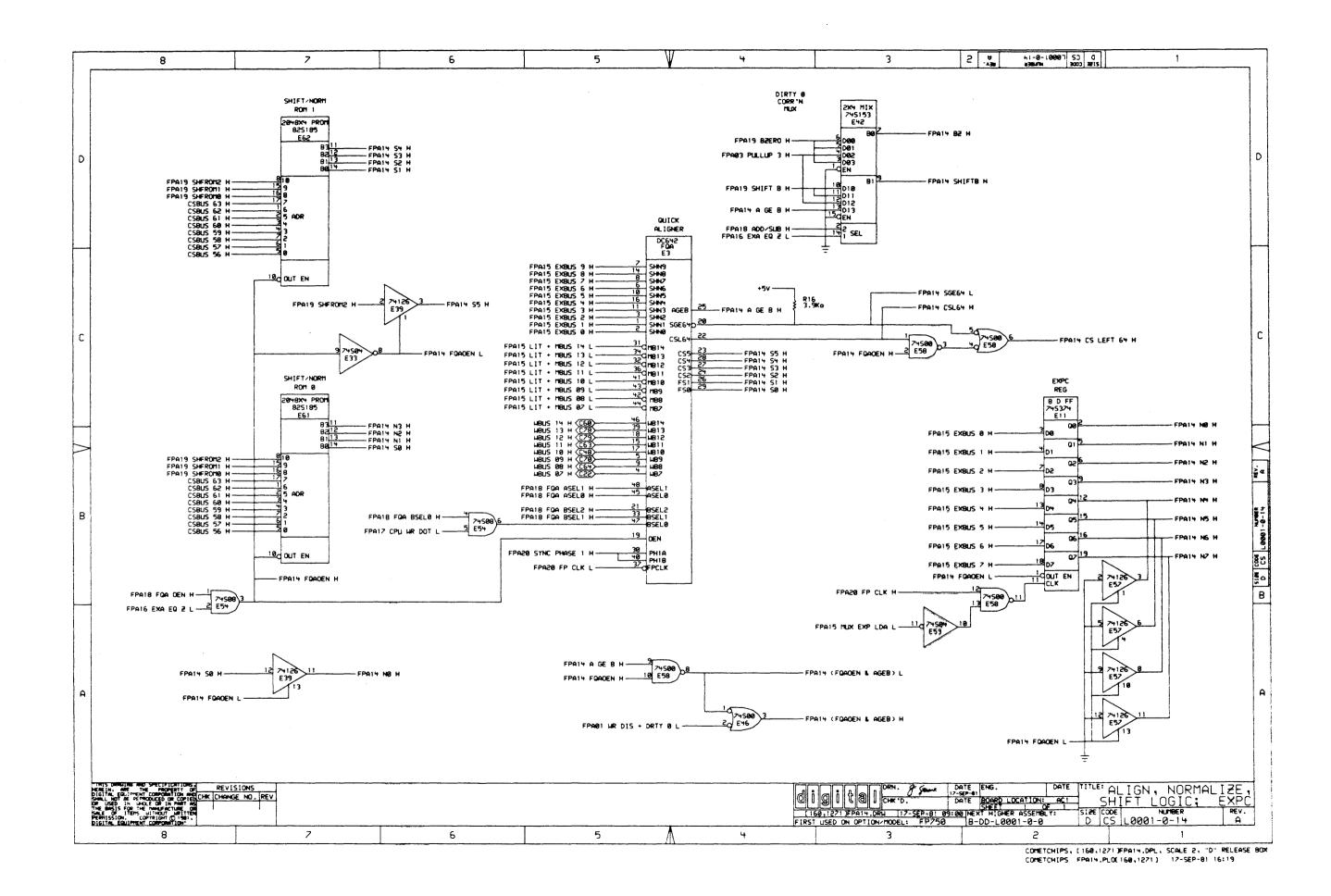


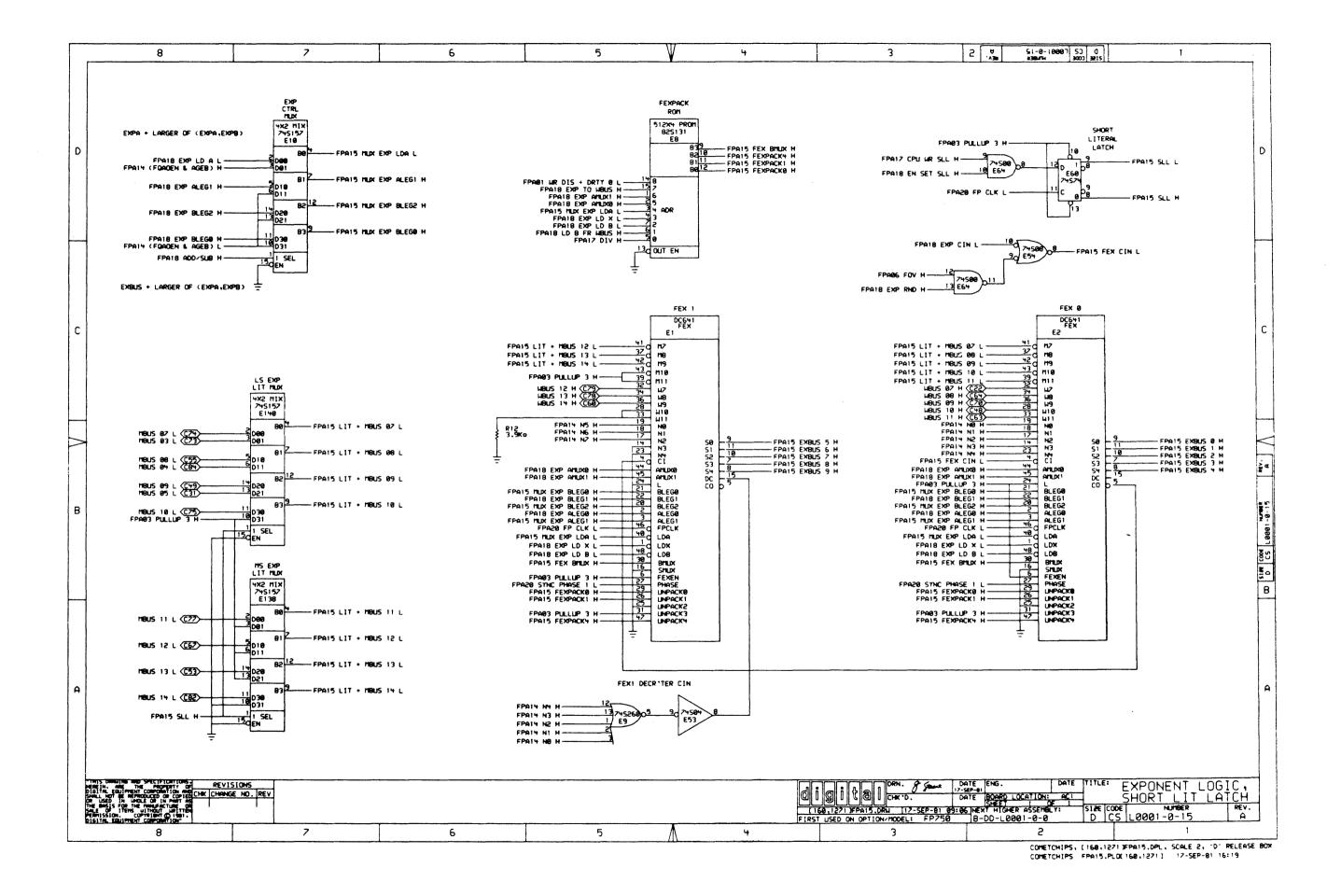


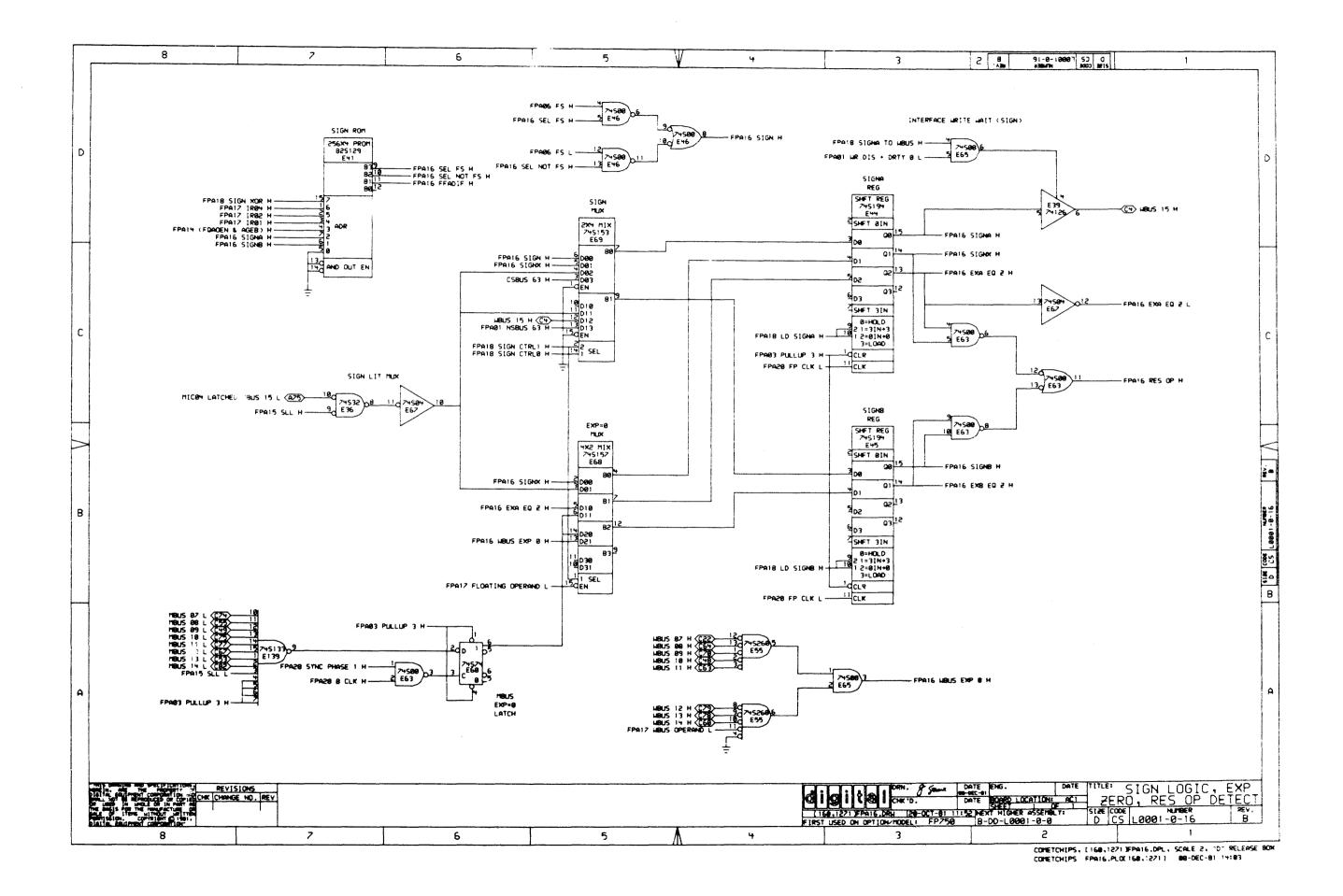


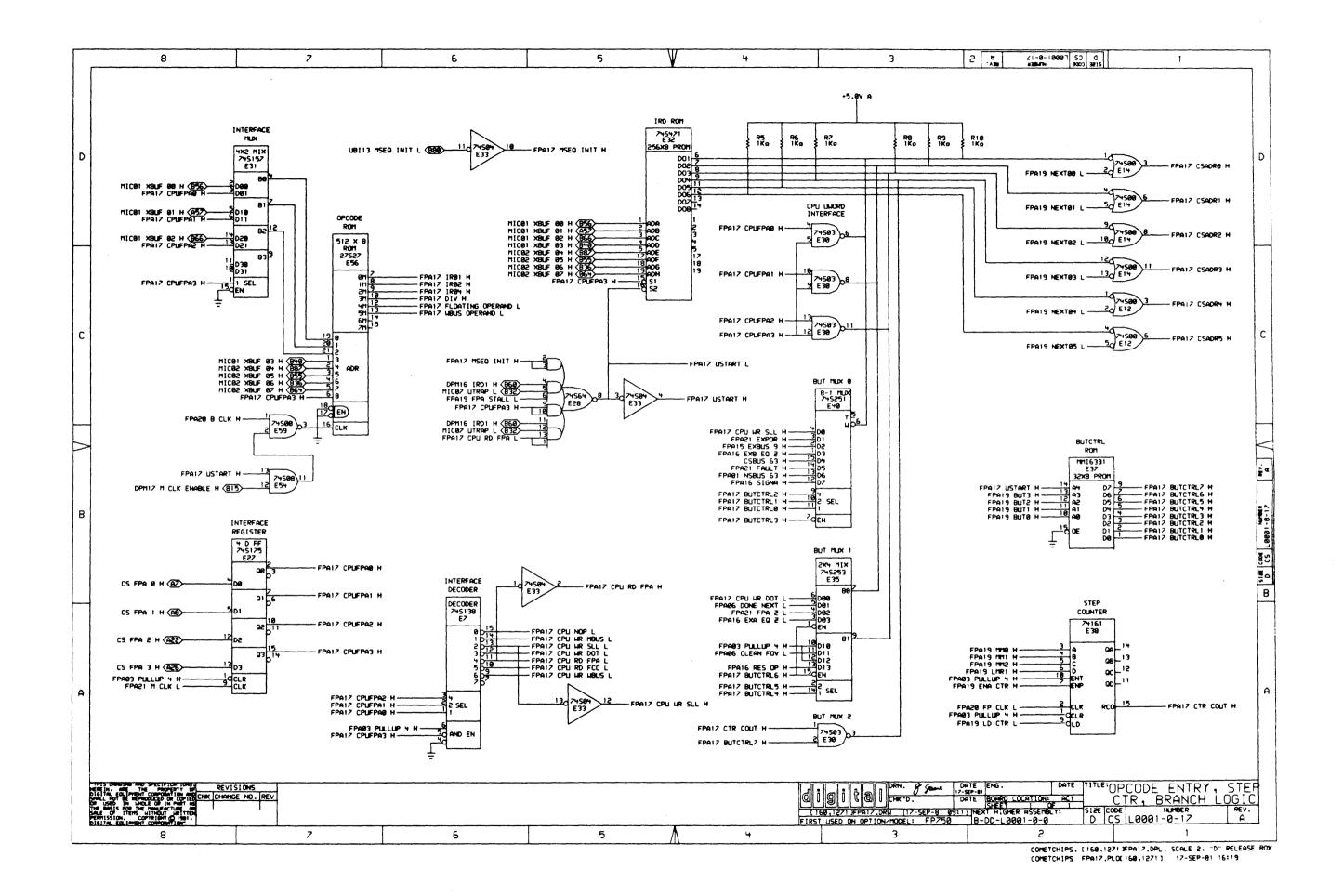


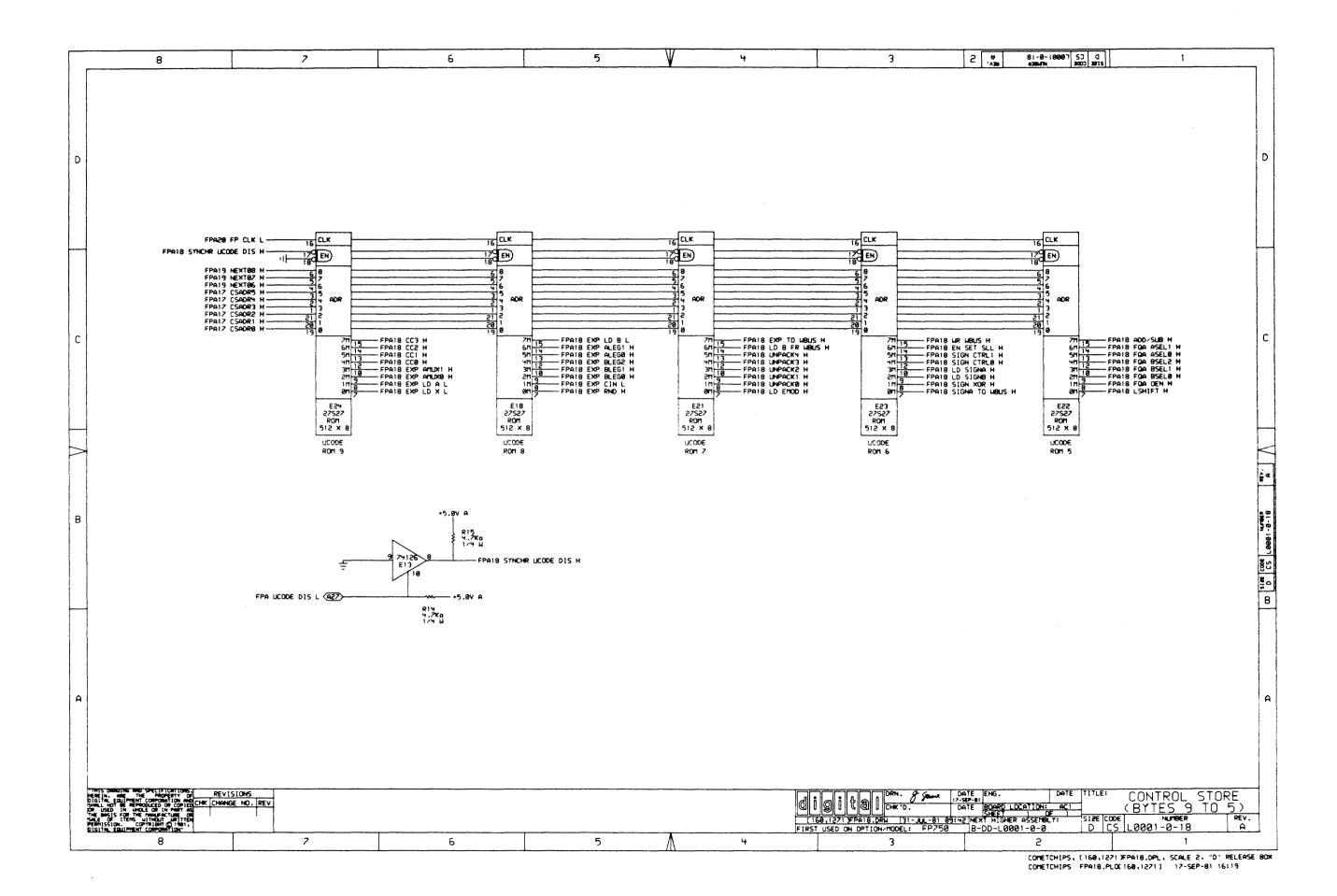


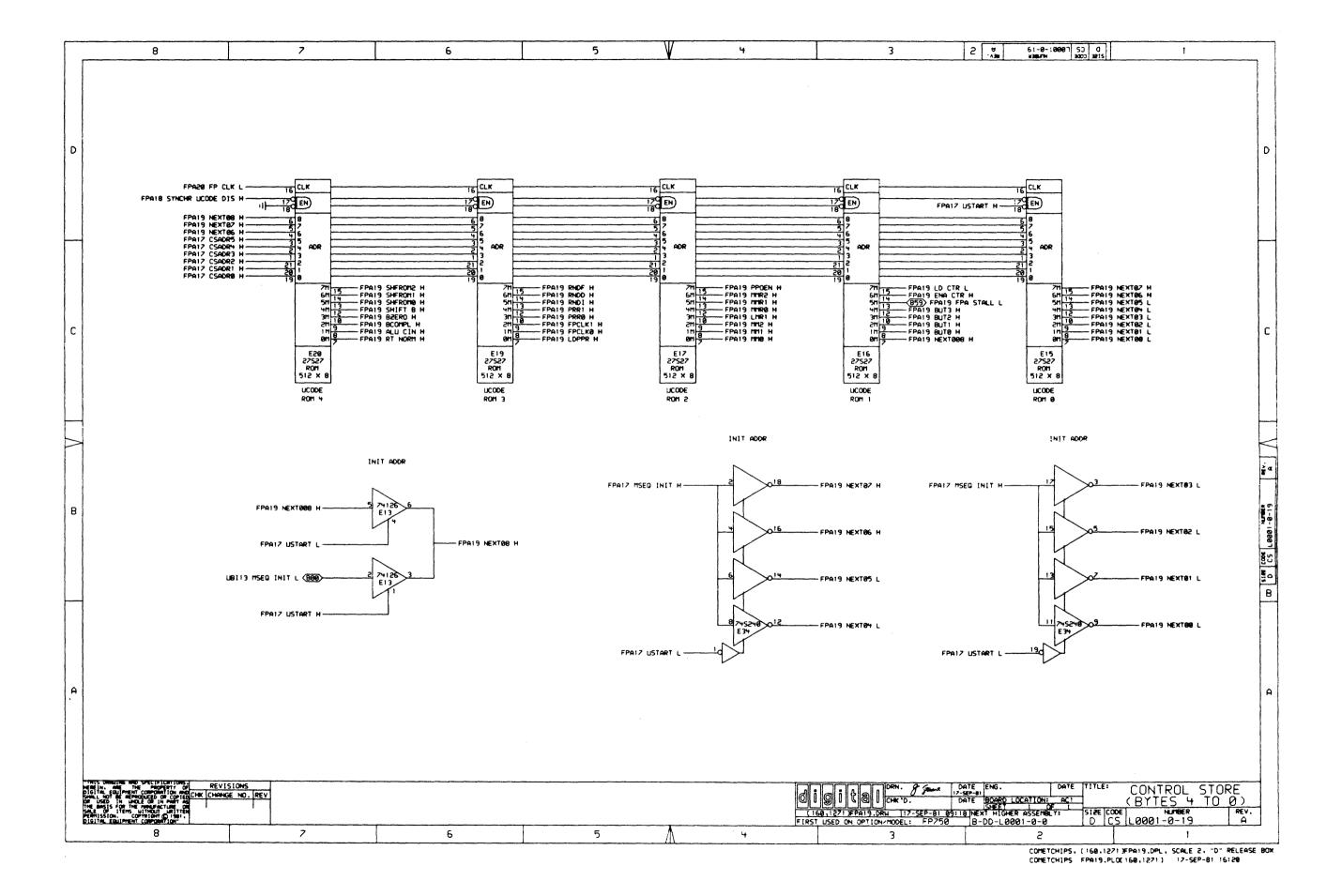


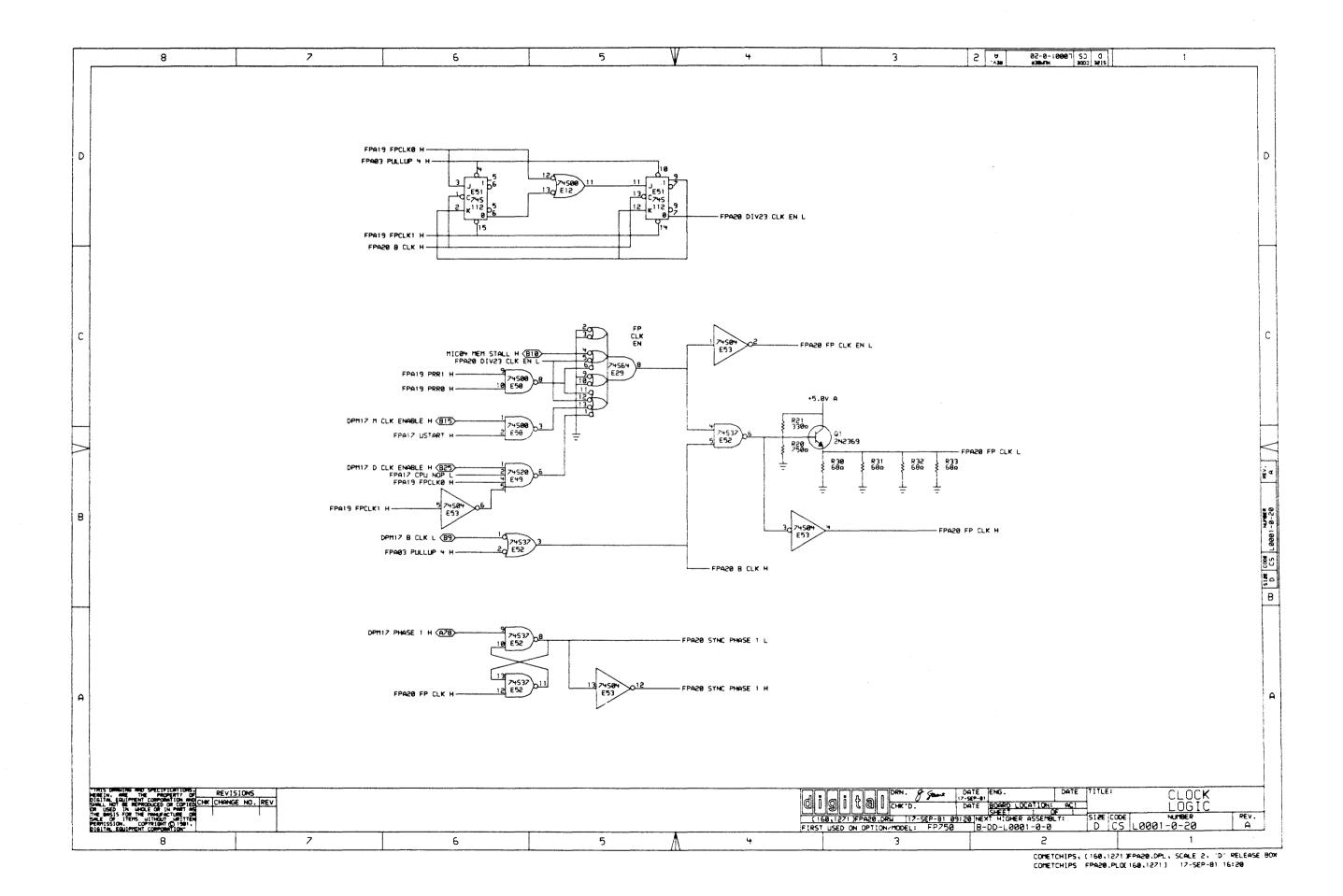


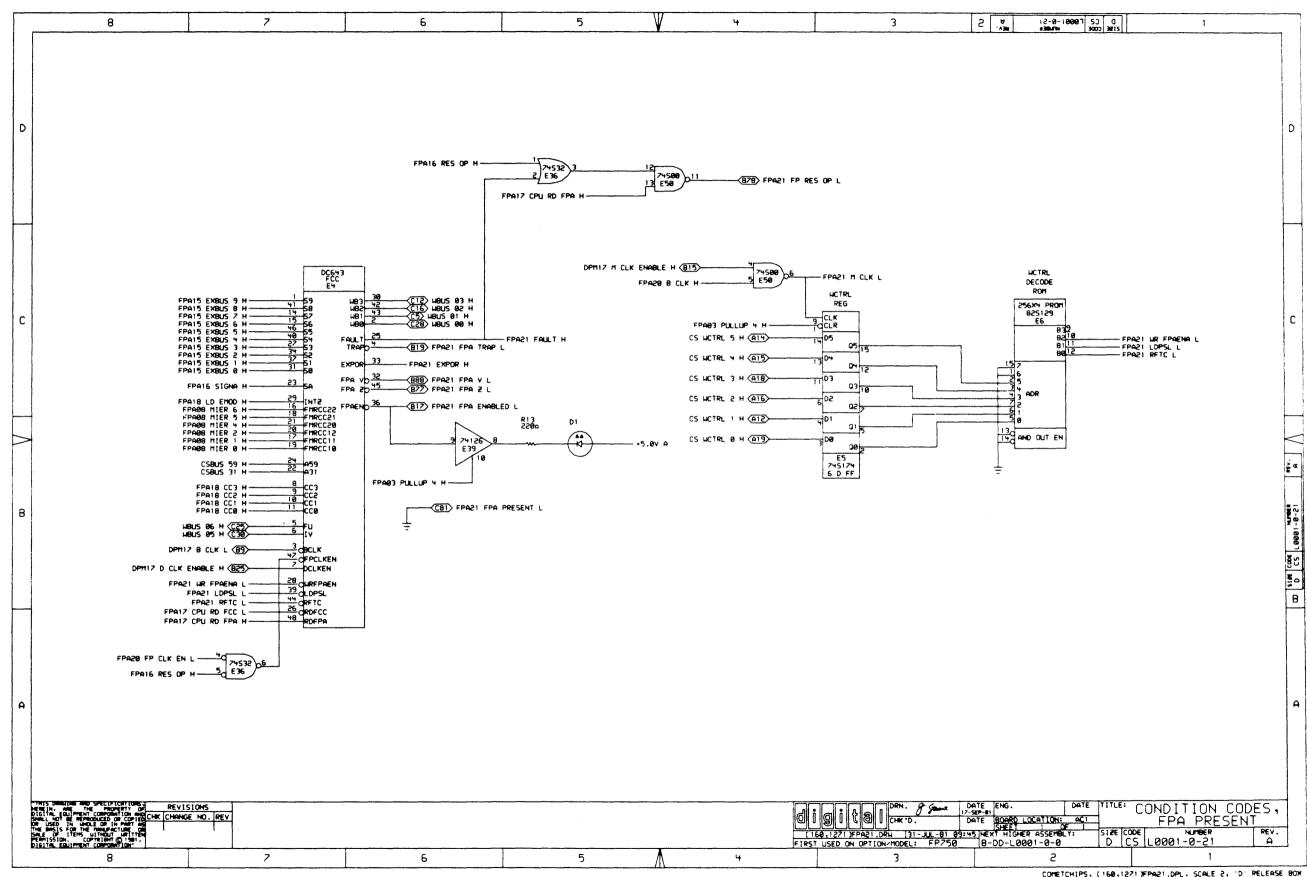


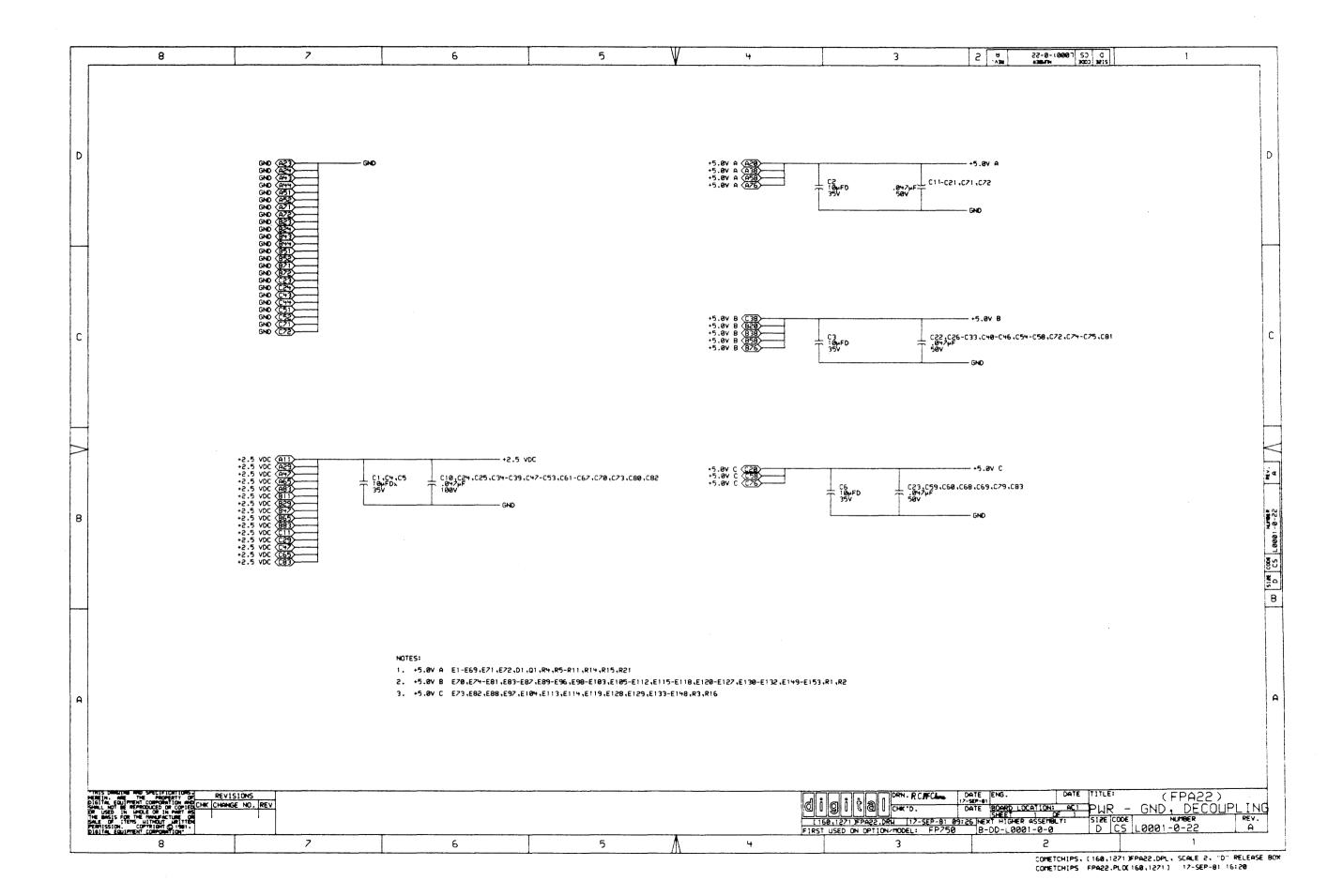






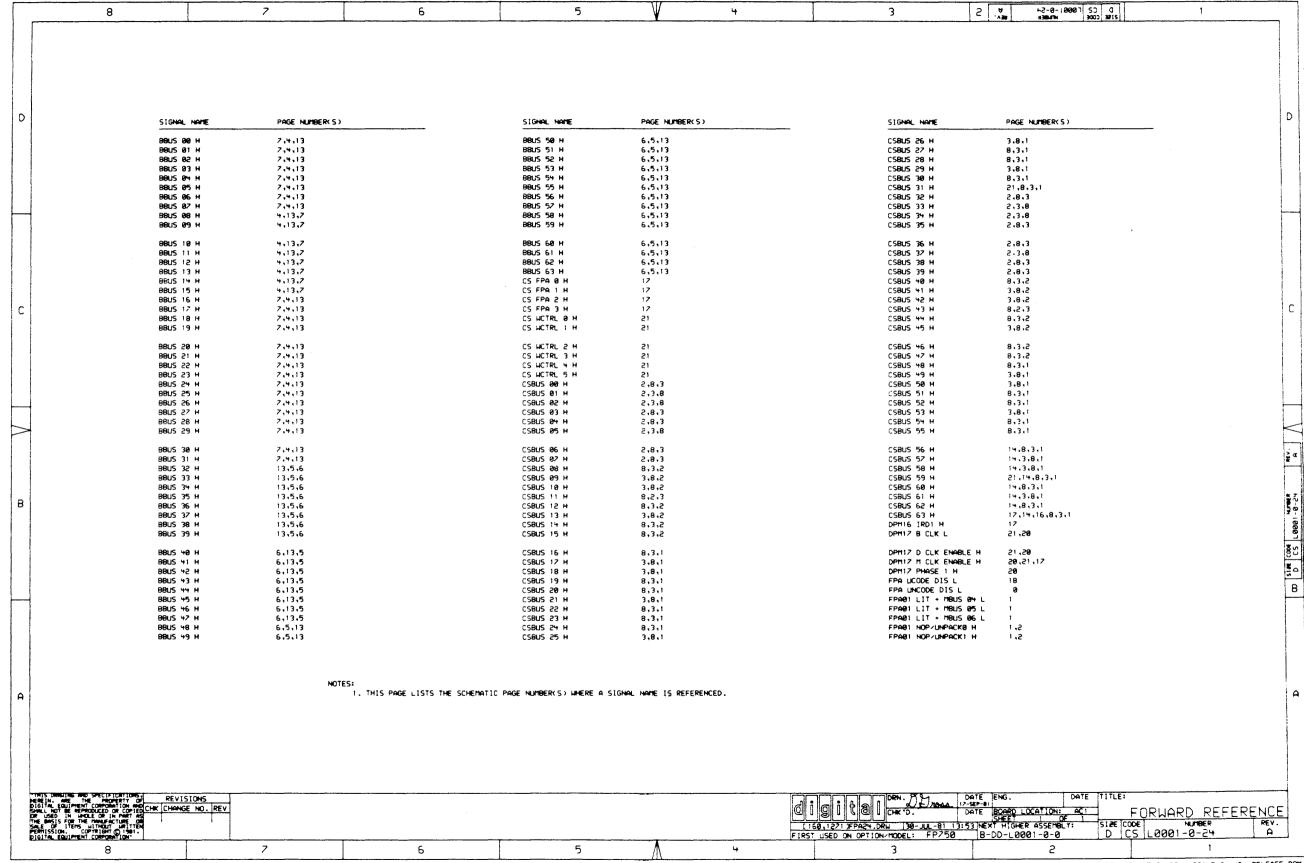






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